

## IN THE CLAIMS

Presented below are all of the pending claims in a clean, unmarked format. Claims 2 and 16 have been cancelled. New claims 19 through 22 have been added. Claims that have not been amended are included with the notation "Unamended".

AB 2/16/02 1  
2 ~~(Once amended) A method, comprising:~~  
3 providing a first resistor with a first end and a second end, said  
4 first end coupled to a switch and said second end coupled to  
5 a data bus wire at a near end of a data bus;  
6 controlling said switch with a detach control signal sent from a far  
7 end of said data bus; and  
switching a biasing voltage from said resistor utilizing said switch.

1 3. (Unamended) The method of claim 1, wherein said first  
2 resistor is configured as a pull-up resistor.

1 4. (Unamended) The method of claim 3, further comprising  
2 detecting said switching of said biasing voltage.

1 5. (Unamended) The method of claim 4, further comprising  
2 determining a logically detached state responsive to said detecting.

1 6. (Unamended) The method of claim 1, wherein said detach  
2 control signal is responsive to a wake-up signal.

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1 7. ~~(Unamended) The method of claim 6, wherein said detach~~  
2 control signal is asserted when said wake-up signal is de-asserted.

1 8. (Once amended) An apparatus, comprising:  
2 a first resistor with a first end and a second end;  
3 a switch coupled to said first end of said first resistor and to a bias  
4 voltage;  
5 a detach control signal wire of a data bus coupled to said switch at  
6 a near end of said data bus, to receive a detach control  
7 signal sent from a far end of said data bus; and  
8 a data bus wire of said data bus coupled to said second end of said  
9 first resistor.

1 9. (Once amended) The apparatus of claim 8, wherein said  
2 switch may apply said bias voltage to said first end of said first resistor  
3 responsively to said detach control signal on said detach control signal  
4 wire.

1 10. (Unamended) The apparatus of claim 9, wherein said detach  
2 control signal is generated responsively to a wake-up signal.

1 11. (Once amended) The apparatus of claim 8, wherein said  
2 data bus carries universal serial bus data.

1 12. (Once amended) The apparatus of claim 8, wherein said  
2 data bus carries IEEE-1394 bus data.

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1 13. (Unamended) The apparatus of claim 8, further comprising  
2 a second resistor with a first end and a second end, said first end  
3 coupled to said data bus wire.

1 14. (Once amended) The apparatus of claim 13, wherein said  
2 second end of said second resistor is coupled to signal ground.

1 15. (Once amended) An apparatus, comprising:  
2 means for providing a first resistor with a first end and a second  
3 end, said first end coupled to a switch and said second end  
4 coupled to a data bus wire at a near end of a data bus;  
5 means for controlling said switch with a detach control signal sent  
6 from a far end of said data bus; and  
7 means for switching a biasing voltage from said resistor utilizing  
8 said switch.

1 17. (Once amended) The apparatus of claim 15, further  
2 comprising  
3 means for detecting said switching of said biasing voltage.

1 18. (Once amended) The apparatus of claim 15, wherein said  
2 detach control signal is responsive to a wake-up signal.

19. ~~(New)~~ A system, comprising:  
a data bus with a near end and a far end;  
a first circuit, coupled to said near end, including a first resistor  
with a first end and a second end, a switch coupled to said first end of  
said first resistor and to a bias voltage, a data bus wire of said data bus  
coupled to said second end of said first resistor, a detach control signal  
wire of said data bus coupled to said switch to receive a detach control  
signal; and  
a second circuit, coupled to said far end, to send said detach  
control signal.

20. (New) The system of claim 19, wherein said switch may  
apply said bias voltage to said first end of said first resistor responsively  
to said detach control signal.

21. (New) The system of claim 20, wherein said detach control  
signal is sent in response to a wake-up signal.

22. (New) The system of claim 21, wherein said wake-up signal  
is sent by said first circuit.